

REMARKS

The claims are claims 1, 11, 12 and 24 to 48.

The application has been amended at many locations to correct minor errors and to present uniform language throughout. The amendments an update of the status of the copending application cited on page 1. Most of the reset of the amendments include correction of an error substituting a capital O for the numeral 0 in some data names.

Claims 1, 11 and 12 are amended. Claims 2 to 10 and 13 to 23 are canceled. New claims 24 to 48 are added. Claims 1 and 12 have been amended to incorporate the utility module taught in the application at page 13, lines 1 to 3 and lines 13 to 15. Claim 11 has been amended to delete reference to the utility module. Claims 24 and 36 recite the utility module includes error checking/calculation as taught in this application at page 13, lines 1 to 3. Claims 25 and 37 recite the utility module includes cyclic redundancy checking as taught in the application at page 13, lines 13 to 15. Claims 26 and 38 recite the utility module includes randomizing as taught in the application at page 13, lines 13 to 15. Claims 27 and 39 recite the utility module includes cyclic address filtering as taught in the application at page 13, lines 13 to 15. Claims 28 and 41 recite the functions of the transmit module taught in the application at page 11, lines 17 to 10. Claims 29 and 42 recite the functions of the receive module taught in the application at page 12, lines 1 to 4. Claims 30 and 43 recite the functions of the deference algorithm module taught in the application at page 11, lines 5 to 8. Claims 31 and 44 recite the functions of the statistical maintenance module taught in the application at page 12, lines 9 to 12. Claims 32 and 45 recite division of the operating modules between as RISC processor and a DSP as illustrated in Figure 23 and taught in the application at

page 41, lines 3 to 11. Claims 33 and 46 recite execution of the operating modules on a DSP as illustrated in Figure 23 and taught in the application at page 41, lines 12 to 16. Claims 34 and 47 recite division of the operating modules between a host processor and a DSP as illustrated in Figure 23 and taught in the application at page 41, line 17 to page 42, line 3. Claims 35 and 48 recite execution of the operating modules and the physical layer interface on a DSP as illustrated in Figure 23 and taught in the application at page 42, lines 4 to 6. Claim 40 corresponds to claim 11 in method form.

Claims 1, 11 and 12 were rejected under 35 U.S.C. 103 (a) as made obvious by Sambamurthy et al U.S. Patent 6,108,713.

Claims 1 and 12 recite subject matter not made obvious by Sambamurthy et al. Claim 1 recites "a utility module programmed to enable at least one utility function employed by more than one of said plurality of operating modules for more than one of said plurality of communication standards." Claim 12 recites "programming a utility module programmed to enable at least one utility function employed by more than one of said plurality of operating modules for more than one of said plurality of communication standards." Sambamurthy et al fails to make obvious such a utility module. The FINAL REJECTION recites at paragraph 12 on page 4 that Sambamurthy et al teaches the utility module in EITHER 112, BACKOFF 216 and DEFER 214 described at column 14, line 36 to column 15, line 60. The Applicants respectfully submit that Sambamurthy et al fails to teach the use of utility functions by more than one operating module in more than one communication standard as recited in claims 1 and 12. The portion of Sambamurthy et al cited in the FINAL REJECTION does not teach that either ETHER 112, BACKOFF 216 or DEFER 214 includes functions used by more than one operating module. Accordingly, claims 1 and 12 are allowable over Sambamurthy et al.

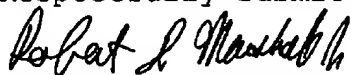
The Applicants respectfully submit that new claims 24 to 48 recite subject matter not made obvious by Sambamurthy et al. Accordingly, these claims should be allowed.

The Applicants respectfully request entry and consideration of this amendment.

The Applicants respectfully submit that all the present claims are allowable for the reasons set forth above. Therefore early entry of this amendment, reconsideration and advance to issue are respectfully requested.

If the Examiner has any questions or other correspondence regarding this application, Applicants request that the Examiner contact Applicants' attorney at the below listed telephone number and address to facilitate prosecution.

Texas Instruments Incorporated
P.O. Box 655474 M/S 3999
Dallas, Texas 75265
(972) 917-5290
Fax: (972) 917-4418

Respectfully submitted,

Robert D. Marshall, Jr.
Reg. No. 28,527